

RESOLUTION IS NOT AUTOMATIZABLE UNLESS $\mathbf{W[P]}$ IS TRACTABLE*

MICHAEL ALEKHNovich

AND ALEXANDER A. RAZBOROV[†]

Abstract. We show that neither Resolution nor tree-like Resolution is automatizable unless the class $\mathbf{W[P]}$ from the hierarchy of parameterized problems is fixed-parameter tractable by randomized algorithms with one-sided error.

Key words. Proof complexity, resolution, automatizability

AMS subject classifications. 03F20, 03D15

1. Introduction. Analysis of the usefulness of proof search heuristics and automated theorem proving procedures based on a proof system P amounts (on the theoretical level) to the following two basic questions:

Question 1. Which theorems in principle possess efficient P -proofs?

Question 2. How to find the optimal (or, at least, a nearly optimal) proof of a given theorem in P ?

Traditional proof complexity mostly dealt, and still deals with the first question. However, there has been a growing interest in the second one, too. An additional motivation to study the complexity of finding optimal proofs comes from deep connections with efficient interpolation theorems; we refer the reader to the surveys [9, 19, 22] for more details. These surveys also serve as a good starting point for learning more about propositional proof complexity in general.

One convenient framework for the theoretical study of Question 2 was proposed in [13]. Namely, they called a proof system P *automatizable* if there exists a deterministic algorithm A which, given a tautology τ , returns its P -proof in time polynomial *in the size of the shortest P -proof of τ* . The definition of a quasi-automatizable proof system is given in the same way, but we only require the algorithm A to run in time which is quasi-polynomial (in the same parameter).

One advantage of this definition is that it allows us to completely disregard the first basic question on the *existence* of efficient P -proofs and indeed concentrate on *finding* efficient proofs *provided* they exist. In particular, the notion of automatizability makes perfect sense for those (weak) proof systems for which hard tautologies are already known. Moreover, the weaker is our system the more likely it seems to be automatizable. One possible explanation of this phenomenon comes from the connection between automatizability and efficient interpolation (every automatizable proof system has efficient interpolation, and the property of having efficient interpolation is indeed anti-monotone w.r.t. the strength of the system). Anyway, given this connection, the results from [20, 13] imply that Extended Frege and TC^0 -Frege proof systems respectively are not automatizable assuming some widely believed cryptographic assumptions. [11] extended the latter result to bounded-depth Frege but under a much stronger assumption.

*A preliminary version of this paper appeared in the *Proceedings of the 42nd IEEE FOCS*, 2001. Supported by the State of New Jersey and NSF grant CCR-9987077.

[†]Institute for Advanced Study, Princeton, US and Steklov Mathematical Institute, Moscow, Russia, razborov@ias.edu.

In this paper we are primarily interested in the automatizability of Resolution and tree-like Resolution. It is worth noting that both systems possess efficient interpolation, therefore their non-automatizability can not be proved via techniques similar to [20, 13, 11]. Nonetheless, [18] proved that it is **NP**-hard to find the shortest resolution refutation. [3] proved that if $\mathbf{P} \neq \mathbf{NP}$ then the length of the shortest resolution refutation can not be approximated to within a constant factor (both for general and tree-like Resolution). Under the stronger assumption $\mathbf{NP} \not\subseteq \mathbf{QP}$ they were able to improve the ratio from an arbitrary constant to $2^{\log^{1-\epsilon} n}$ (later [16] obtained a better PCP characterization of **NP** that allows to prove the same bound for arbitrary $\epsilon \rightarrow 0$ modulo $\mathbf{P} \neq \mathbf{NP}$).

In the opposite direction, [8] observed that tree-like Resolution is quasi-automatizable. Thus, it is unlikely to show that this system is not automatizable modulo $\mathbf{P} \neq \mathbf{NP}$ conjecture, because it would imply quasi-polynomial algorithms for **NP** (in case of general Resolution this goal seems also tricky at the moment because there is only one¹ known example [12] for which the proof search algorithm of [10] requires more than quasi-polynomial time). Therefore, any result establishing non-automatizability of tree-like Resolution needs to be formulated within a complexity framework in which the asymptotics $n^{O(1)}$ and $n^{\log n}$ are essentially different.

One natural example of such a framework is *parameterized complexity* introduced by Downey and Fellows (see [17]) in which algorithms working in time $f(k)n^{O(1)}$ and n^k are considered different from the point of view of effectiveness (here k is an integer input parameter that is supposed to be an “arbitrarily large” constant). In this paper we prove that neither Resolution nor tree-like Resolution is automatizable unless the class $\mathbf{W[P]}$ (lying very high in the hierarchy of parameterized problems) is fixed-parameter tractable by a randomized algorithm with one-sided error (Theorem 2.7). Our proof goes by a reduction from the optimization problem MINIMUM MONOTONE CIRCUIT SATISFYING ASSIGNMENT (MMCSA for short) whose decision version is complete for the class $\mathbf{W[P]}$. An alternative hardness assumption is that there is no *deterministic* fixed-parameter algorithm which *approximates* MMCSA within any constant factor (Theorem 2.5). It is worth noting in this connection that we were able to relate to each other the hardness of finding *exact* and *approximate* solutions for MMCSA without using the PCP Theorem (see the proof of Theorem 2.7 given in Section 4). This result can be interesting in its own.

The paper is organized as follows. Section 2 contains necessary preliminaries and definitions, in Section 3 we present our core reduction from MMCSA to automatizability of Resolution, and in Section 4 we use (sometimes non-trivial) self-improving techniques to prove our main results, Theorems 2.5 and 2.7. The paper is concluded with some open problems in Section 5.

1.1. Recent developments. Since the preliminary version of this paper was released, the following related developments have occurred.

Atserias and Bonet [6] studied a slightly different variant of automatizability that they called *weak automatizability*. Using their techniques, they were also able to produce more examples of poly-size tautologies for which the width based proof search algorithm from [10] requires more than quasi-polynomial time. In the opposite direction, Alekhovich and Razborov [4] introduced an enhancement of that algorithm called by them BWBASP (for Branch-Width Based Automated Theorem Prover).

¹See, however, Section 1.1 below.

This algorithm performs better than the width based algorithm for several important classes of tautologies, and for at least one such class it even achieves complete (that is, polynomial) automatization. Finally, quite unexpectedly our techniques turned out to be useful in the totally different area of computational learning, where they inspired a number of strong hardness results for the so-called model of proper learning [2].

2. Preliminaries and main results.

2.1. Resolution and automatizability. Let x be a Boolean variable, i.e. a variable that ranges over the set $\{0, 1\}$. A *literal* of x is either x (denoted sometimes as x^1) or \bar{x} (denoted sometimes as x^0). A *clause* is a disjunction of literals. A *CNF* is a conjunction of pairwise different clauses.

Let $f(x_1, \dots, x_n)$ be an arbitrary function (possibly, partial) from $\{0, 1\}^n$ to some finite domain D . An *assignment* to f is a mapping $\alpha : \{x_1, \dots, x_n\} \rightarrow \{0, 1\}$. A *restriction* of f is a mapping $\rho : \{x_1, \dots, x_n\} \rightarrow \{0, 1, \star\}$. We denote by $|\rho|$ the number of assigned variables, $|\rho| \stackrel{\text{def}}{=} |\rho^{-1}(\{0, 1\})|$. The *restriction of a function f or CNF τ by ρ* , denoted by $f|_\rho$ [$\tau|_\rho$] is the function [CNF] obtained from f [τ , respectively] by setting the value of each $x \in \rho^{-1}(\{0, 1\})$ to $\rho(x)$, and leaving each $x \in \rho^{-1}(\star)$ as a variable.

The general definition of a propositional proof system was given in the seminal paper [15]. But since we are interested only in Resolution (which is one of the simplest and most widely studied concrete systems), we prefer to skip this general definition. *Resolution* operates with clauses and has one rule of inference called *resolution rule*:

$$\frac{A \vee x \quad B \vee \bar{x}}{A \vee B}.$$

A resolution proof is *tree-like* if its underlying graph is a tree. A *resolution refutation* of a CNF τ is a resolution proof of the empty clause from the clauses appearing in τ .

The *size* of a resolution proof is the overall number of clauses in it. For an unsatisfiable CNF τ , $S(\tau)$ [$S_T(\tau)$] is the minimal size of its resolution refutation [tree-like resolution refutation, respectively]. Clearly, $S(\tau) \leq S_T(\tau)$.

The *width* $w(C)$ of a clause C is the number of literals in C . The *width* $w(\tau)$ of a set of clauses τ (in particular, the width of a resolution proof) is the maximal width of a clause appearing in this set. For a CNF τ , let $n(\tau)$ be the overall number of distinct variables appearing in it, and let $|\tau|$ be the overall number of occurrences of variables in τ , i.e., $|\tau| \stackrel{\text{def}}{=} \sum_{C \in \tau} w(C)$. For an unsatisfiable CNF τ , $w(\tau \vdash \emptyset)$ will denote the minimal width of its resolution refutation.

For a non-negative integer n , let $[n] \stackrel{\text{def}}{=} \{1, 2, \dots, n\}$, and let $[n]^k \stackrel{\text{def}}{=} \{I \subseteq [n] \mid |I| = k\}$.

We will recall the general definition of automatizability from [13] for the special cases of Resolution and tree-like Resolution.

DEFINITION 2.1. *Resolution [tree-like Resolution] is (quasi-)automatizable if there exists a deterministic algorithm A which, given an unsatisfiable CNF τ , returns its resolution refutation [tree-like resolution refutation, respectively] in time which is (quasi-)polynomial in $|\tau| + S(\tau)$ [$|\tau| + S_T(\tau)$, respectively].*

REMARK 1. *Note that we do not require that all clauses from τ must necessarily appear in the refutation, therefore, we can not a priori expect the inequality $n(\tau) \cdot S(\tau) \geq |\tau|$. This is why we must introduce the term $|\tau|$ into the bound on the running time of A when adapting the general definition of automatizability from [13] to the case of Resolution.*

2.2. Parameterized complexity and MMCSA problem. We refer the reader to [17] for a good general introduction to the topic of parameterized complexity.

DEFINITION 2.2 ([17, Definition 2.4]). *The class **FPT** (that stands for fixed parameter tractable) of parameterized problems consists of all languages $L \subseteq \Sigma^* \times \mathbb{N}$ for which there exists an algorithm Φ , a constant c and a recursive function $f : \mathbb{N} \rightarrow \mathbb{N}$ such that:*

1. *the running time of $\Phi(\langle x, k \rangle)$ is at most $f(k) \cdot |x|^c$;*
2. *$\langle x, k \rangle \in L$ iff $\Phi(\langle x, k \rangle) = 1$.*

Thus, an algorithm is considered to be feasible if it works in time polynomial in $n \stackrel{\text{def}}{=} |x|$ and $f(k)$, where k is supposed to be much smaller than n , and f is an arbitrarily large (recursive) function. A similar feasibility requirement arises in the theory of polynomial time approximation schemes (PTAS) for **NP**-hard problems: assume that we have an algorithm that approximates a given problem within arbitrary error $\epsilon > 0$ working in time $n^{O(1/\epsilon)}$. Is it possible to get rid of $1/\epsilon$ in the exponent and do it in time $f(1/\epsilon)n^{O(1)}$ (the algorithms which obey the latter bound on the running time are called EPTAS, efficient polynomial time approximation schemes)?

It turns out that this question is tightly related to the fixed parameter tractability. Namely, the existence of EPTAS for a given problem implies an *exact* algorithm for the corresponding fixed parameter version (see [7, 14]).

To study the complexity of parameterized problems, the following *parameterized reduction* (that preserve the property of being in **FPT**) is used:

DEFINITION 2.3 ([17, Definition 9.3]). *A parameterized problem $L \subseteq \Sigma^* \times \mathbb{N}$ reduces to another parameterized problem $L' \subseteq \Sigma^* \times \mathbb{N}$ if there exist (arbitrary!) functions $f, g : \mathbb{N} \rightarrow \mathbb{N}$, and a function $h : \Sigma^* \times \mathbb{N} \rightarrow \Sigma^*$ such that $h(x, k)$ is computable in time $f(k)|x|^{O(1)}$, and $\langle x, k \rangle \in L$ iff $\langle h(x, k), g(k) \rangle \in L'$. For any integer t , the parameterized problem WEIGHTED t -NORMALIZED SATISFIABILITY is defined by restricting the ordinary SATISFIABILITY to a certain class of Boolean formulas depending on t (we omit the exact definition since it is a little bit technical and not needed for our results), and the parameter k bounds the Hamming weight of the satisfying assignment we are searching for. The complexity class **W**[t] consists of all problems that can be reduced to WEIGHTED- t -NORMALIZED-SATISFIABILITY via parameterized reduction, and the class **W**[**P**] (where P stands for polynomial) includes all problems reducible to WEIGHTED CIRCUIT SATISFIABILITY described as:*

WEIGHTED CIRCUIT SATISFIABILITY:

Instance: A circuit C .

Parameter: A positive integer k .

Question: Does C have a satisfying assignment of Hamming weight (defined as the number of ones) k ?

These definitions lead to the following *parameterized hierarchy*, in which every inclusion is believed to be strict:

$$\mathbf{FPT} \subseteq \mathbf{W}[1] \subseteq \mathbf{W}[2] \dots \subseteq \mathbf{W}[\mathbf{P}].$$

In our paper we construct a randomized parameterized reduction from the automatizability of Resolution to the following optimization problem (MMCSA in what follows) that was introduced in [3].

Monotone Minimum Circuit Satisfying Assignment:

Instance: A monotone circuit C in n variables over the basis $\{\wedge, \vee\}$.

Solution: An assignment $a \in \{0, 1\}^n$ such that $C(a) = 1$.

Objective function: $k(a)$, defined as its Hamming weight.

By $k(C)$ we will denote the minimal value $k(a)$ of a solution a for an instance C of MMCSA.

The following easy observation was made in [3] (“self-improvement”).

PROPOSITION 2.4. *For every fixed integer $d > 0$ there exists a poly-time computable function π which maps monotone circuits into monotone circuits and such that $k(\pi(C)) = k(C)^d$ for all C .*

Our first result can be now formulated as follows:

THEOREM 2.5. *If either Resolution or tree-like Resolution is automatizable then for any fixed $\epsilon > 0$ there exists an algorithm Φ receiving as inputs monotone circuits C which runs in time $\exp(k(C)^{O(1)}) \cdot |C|^{O(1)}$ and approximates the value of $k(C)$ to within a factor $(1 + \epsilon)$.*

The decision version of MMCSA was considered in [17] (under the name WEIGHTED MONOTONE CIRCUIT SATISFIABILITY) in the context of parameterized complexity and was shown to be complete in the class **W[P]**.

In order to formulate our second (and main) result, we need to introduce the obvious hybrid of the classes **R** and **FPT**:

DEFINITION 2.6. *The class **FPR** of parameterized problems consists of all languages $L \subseteq \Sigma^* \times \mathbb{N}$ for which there exists a probabilistic algorithm Φ , a constant c and a recursive function $f : \mathbb{N} \rightarrow \mathbb{N}$ such that:*

1. *the running time of $\Phi(\langle x, k \rangle)$ is at most $f(k) \cdot |x|^c$;*
2. *if $\langle x, k \rangle \in L$ then $\mathbf{P}[\Phi(\langle x, k \rangle) = 1] \geq 1/2$;*
3. *if $\langle x, k \rangle \notin L$ then $\mathbf{P}[\Phi(\langle x, k \rangle) = 1] = 0$.*

Then we have:

THEOREM 2.7. *If either Resolution or tree-like Resolution is automatizable then $\mathbf{W[P]} \subseteq \text{co-FPR}$.*

3. Main reduction from MMCSA to automatizability of Resolution.

This section is entirely devoted to the proof of the following technical lemma.

LEMMA 3.1. *There exists a poly-time computable function τ which maps any pair $\langle C, 1^m \rangle$, where C is a monotone circuit and m is an integer, to an unsatisfiable CNF $\tau(C, m)$ such that:*

$$S_T(\tau(C, m)) \leq |C| \cdot m^{O(\min\{k(C), \log m\})}$$

and

$$S(\tau(C, m)) \geq m^{\Omega(\min\{k(C), \log m\})}. \quad (3.1)$$

We begin the proof of Lemma 3.1 by describing CNFs that form the main building block $\tau(C, m)$ and establishing their necessary properties. From now on fix a monotone circuit C in n variables p_1, \dots, p_n . Let $\mathcal{A} \subseteq \{0, 1\}^m$. We will call vectors from \mathcal{A} (usually represented as columns) *admissible* and call an 0-1 matrix with m rows \mathcal{A} -*admissible* if all its columns are so. Consider the following combinatorial principle $\mathcal{P}_{C, \mathcal{A}}$ (that may be true or false, depending on the choice of C and \mathcal{A}):

$\mathcal{P}_{C, \mathcal{A}}$: *every $(m \times n)$ 0-1 \mathcal{A} -admissible matrix $A = (a_{ij})$ contains a row $i \in [m]$ such that $C(a_{i1}, a_{i2}, \dots, a_{in}) = 1$.*

Let us formulate one sufficient condition for $\mathcal{P}_{C,\mathcal{A}}$ to be true (regardless of proof complexity considerations.)

DEFINITION 3.2. $d_1(\mathcal{A})$ is the maximal d such that for every d vectors from \mathcal{A} there exists a position $i \in [m]$ in which all these vectors have 1. $d_1(\mathcal{A})$ can be also easily characterized in terms of minimum covers. Namely, if we associate with

every $\begin{pmatrix} a_1 \\ \vdots \\ a_m \end{pmatrix} \in \mathcal{A}$ the subset $\{i \in [m] \mid a_i = 0\}$ of $[m]$ then $d_1(\mathcal{A}) + 1$ is exactly the minimal number of such sets needed to cover the whole $[m]$.

LEMMA 3.3. If $k(C) \leq d_1(\mathcal{A})$ then $\mathcal{P}_{C,\mathcal{A}}$ is true.

Proof. Let A be an $(m \times n)$ 0-1 \mathcal{A} -admissible matrix. Let $a = (a_1, \dots, a_n)$ be such that $C(a_1, \dots, a_n) = 1$ and $k(a) = k(C)$. Let $\mathcal{A}_0 \stackrel{\text{def}}{=} \left\{ \begin{pmatrix} a_{1j} \\ \vdots \\ a_{mj} \end{pmatrix} \mid a_j = 1 \right\}$ be the set of all columns in A corresponding to those positions j for which $a_j = 1$. Since $|\mathcal{A}_0| \leq k(a) = k(C) \leq d_1(\mathcal{A})$, there exists $i \in [m]$ such that $a_{ij} = 1$ whenever $a_j = 1$. This means $a_{ij} \geq a_j$ for all $j \in [n]$ and implies $C(a_{i1}, \dots, a_{in}) = 1$ since C is monotone. \square

The proof of Lemma 3.3 suggests that if C and \mathcal{A} with the property $k(C) \leq d_1(\mathcal{A})$ are “generic enough”, then the optimal propositional proof of the principle $\mathcal{P}_{C,\mathcal{A}}$ should exhaustively search through all $|\mathcal{A}|^{k(C)}$ possible placements of admissible vectors to the columns $\{j \mid a_j = 1\}$ and thus have size roughly $|\mathcal{A}|^{k(C)}$. Our task is to find an encoding of (the negation of) $\mathcal{P}_{C,\mathcal{A}}$ as a CNF so that we can prove tight upper and lower bounds on $S_T(\tau(C, \mathcal{A}))$ and $S(\tau(C, \mathcal{A}))$ of (roughly) this order. This encoding is somewhat technical and involves several auxiliary functions (see Definition 3.4 below). In order to convey some intuition why do we need all of these, let us briefly discuss two “naive” attempts at a simpler proof.

Attempt 1 (no encoding at all.)

Suppose that we simply enumerate elements of \mathcal{A} by binary strings of length $\log |\mathcal{A}|$, and introduce propositional variables expressing their bits. The main problem with this encoding is that it does not behave well with respect to (random) restrictions. The standard width-reducing argument from [8] that we use in part 3 of Lemma 3.8 below assumes a “reasonably uniform” distribution on the set of those restrictions that “reasonably preserve” the complexity of the tautology. But with the straightforward encoding, any restriction of propositional variables used for enumerating the set \mathcal{A} results in shrinking this set and completely destroys its useful properties.

We circumvent this in a standard way, by using “excessive encodings” $F_1, \dots, F_n : \{0, 1\}^s \rightarrow \mathcal{A}$, where $F_i(x_1, \dots, x_s)$ are surjective and remain so after restricting not too many variables (Definition 3.5). It is worth noting that even if we may have assumed in our definition of $\tau(C, \mathcal{A})$ that $F_1 = F_2 = \dots = F_n$, this property will *not* be invariant under restrictions (and this is why it is more convenient not to make this assumption).

Attempt 2 (same encoding for \mathcal{A} and C .)

The naive encoding of the circuit C (that is, by propositional variables z_{iv} encoding the intermediate result of the computation by the circuit C at the gate v when its input is the i th row of A) suffers from the same drawback as above: we do not want the values of z_{iv} to be exposed by a random restriction. But why don’t we apply to

the variables z_{iv} just the same excessive encodings we used above for the elements of \mathcal{A} ?

It turns out that with this “lighter” version our lower bounds already go through, and the upper bound holds for *general* Resolution (in particular, the reader interested only in this case can safely assume this simplification). In the tree-like case, however, the upper bound becomes problematic. Namely, when formalizing the proof of Lemma 3.3, we need to prove the fact $C(a_{i_1}, \dots, a_{i_n}) = 1$, and the natural way of doing this in *tree-like* Resolution assumes full access to clauses of the form $(z_{i,v_1} \wedge \dots \wedge z_{i,v_\mu} \supset z_{i,v})$ (cf. the proof of part 1 in Lemma 3.8). This is not a problem if the variables $z_{i,v}$ are not encoded, but if we encode them in a non-trivial way, then we no longer will have a resolution proof.

In order to balance between these two conflicting requirements, we introduce a more sophisticated encoding scheme that intuitively looks as follows. Imagine that we have many independent copies C_1, \dots, C_r of the circuit C ; indices $c \in \{1, 2, \dots, r\}$ will be called *controls*. The (unencoded!) variables $z_{i,v}^c$ will again express the protocol of computing the value $C_c(a_{i_1}, \dots, a_{i_n})$. But for every individual row $i \in [m]$, our axioms will require this protocol to be valid only for *one* of these r circuits (say, C_{c_i}), and the values c_i are excessively encoded by surjective mappings $f_1, \dots, f_m : \{0, 1\}^s \rightarrow [r]$ in the same way as we did with the elements of \mathcal{A} .

In order to not obstruct the proof with irrelevant details, we will define our CNFs $\tau(C, \mathcal{A}, \vec{F}, \vec{f})$ and establish their necessary properties in a situation which is more general than what will be actually needed for completing the proof of Lemma 3.1 (see page 11). If the reader prefers, he/she may think during the course of the proof that \mathcal{A} is an arbitrary set of vectors such that $d_1(\mathcal{A}) \geq \Omega(\log m)$ and (see Definition 3.6) $d_0(\mathcal{A}) \geq \Omega(\log m)$. Furthermore, $r = \log m$, $s = O(\log m)$ and F_j, f_i will be $(\log m)$ -surjective in the sense of Definition 3.5.

DEFINITION 3.4. Let $C(p_1, \dots, p_n)$ be a monotone circuit, $\mathcal{A} \subseteq \{0, 1\}^m$ be a set of vectors and $F_1, \dots, F_n : \{0, 1\}^s \rightarrow \mathcal{A}$, $f_1, \dots, f_m : \{0, 1\}^s \rightarrow [r]$ be surjective functions, where f_i s are possibly partial. For every $j \in [n]$ and $\nu \in [s]$ we introduce a propositional variable x_j^ν , for every $i \in [m]$ and $\nu \in [s]$ introduce a variable y_i^ν , and for every $i \in [m]$, every $c \in [r]$ (elements of this set will be sometimes referred to as controls) and every vertex v of the circuit C introduce a variable $z_{i,v}^c$.

For $j \in [n]$ and $\vec{a} \in \mathcal{A}$, let us denote by $[Column_j = \vec{a}]$ the predicate $F_j(x_j^1, \dots, x_j^s) = \vec{a}$. Likewise, for $i \in [m]$ and $c \in [r]$, let $[Control_i = c]$ denote the predicate “ $f_i(y_i^1, \dots, y_i^s) = c$ ”.

The CNF $\tau(C, \mathcal{A}, \vec{F}, \vec{f})$ consists of all clauses that result from the expansion of the following Boolean predicates as CNFs:

$$(y_i^1, \dots, y_i^s) \in \text{dom}(f_i), \text{ for all } i \in [m]; \quad (3.2)$$

$$\left. \begin{aligned} & ([Column_j = \vec{a}] \wedge [Control_i = c]) \supset z_{i,p_j}^c \\ & \text{for all } \vec{a} \in \mathcal{A}, i \in [m] \text{ such that } a_i = 1 \text{ and all } j \in [n], c \in [r]; \end{aligned} \right\} \quad (3.3)$$

$$\left. \begin{array}{l} ([Control_i = c] \wedge (z_{i,v'}^c * z_{i,v''}^c)) \supset z_{iv}^c \\ \text{for all } i \in [m], c \in [r] \text{ and all internal nodes } v \\ \text{corresponding to the instruction } v \leftarrow v' * v'', * \in \{\wedge, \vee\}; \end{array} \right\} \quad (3.4)$$

$$[Control_i = c] \supset \bar{z}_{i,v_{\text{fin}}}, \text{ where } v_{\text{fin}} \text{ is the output node of } C. \quad (3.5)$$

It is easy to see that $\tau(C, \mathcal{A}, \vec{F}, \vec{f})$ is unsatisfiable (for arbitrary surjective \vec{F}, \vec{f}) if and only if $P_{C,\mathcal{A}}$ is true. Also, as we already mentioned, the only thing we need from \vec{F}, \vec{f} is that they remain surjective after restricting a few variables.

DEFINITION 3.5. We say that an onto (possibly partial) function $g : \{0, 1\}^s \longrightarrow D$ is r -surjective if for any restriction ρ with $|\rho| \leq r$ the function $g|_\rho$ is still onto.

Finally, for the lower bound we need a notion dual to $d_1(\mathcal{A})$.

DEFINITION 3.6. $d_0(\mathcal{A})$ is the maximal d such that for every d positions $i_1, \dots, i_d \in [m]$ there exists $\vec{a} \in \mathcal{A}$ such that $a_{i_1} = \dots = a_{i_d} = 0$.

Now we are ready to formulate our main technical lemma that provides upper and lower bounds on the size of optimal resolution refutations of $\tau(C, \mathcal{A}, \vec{F}, \vec{f})$. Like many similar proofs in the area, the lower bound is naturally split into two fairly independent parts. The first part provides lower bounds on $w(\tau(C, \mathcal{A}, \vec{F}, \vec{f}) \vdash \emptyset)$, but for technical reasons we need a slightly stronger statement based on a modified notion of width.

DEFINITION 3.7. For a clause D in the variables of the CNF $\tau(C, \mathcal{A}, \vec{F}, \vec{f})$, let $w_x(D), w_y(D)$ and $w_c(D)$ ($c \in [r]$) be the numbers of x -variables, y -variables and z -variables of the form $z_{i,v}^c$, respectively, appearing in D . We define the controlled width $\tilde{w}(D)$ as

$$\tilde{w}(D) \stackrel{\text{def}}{=} w_x(D) + w_y(D) + r \cdot \min_{c \in [r]} w_c(D).$$

The minimal controlled width $\tilde{w}(\tau(C, \mathcal{A}, \vec{F}, \vec{f}) \vdash \emptyset)$ is defined similarly to the minimal refutation width.

Clearly, $\tilde{w}(D) \leq w(D)$ for any clause D , and thus $\tilde{w}(\tau(C, \mathcal{A}, \vec{F}, \vec{f}) \vdash \emptyset) \leq w(\tau(C, \mathcal{A}, \vec{F}, \vec{f}) \vdash \emptyset)$.

LEMMA 3.8. Let C be a monotone circuit in n variables, $\mathcal{A} \subseteq \{0, 1\}^m$ and $F_1, \dots, F_n : \{0, 1\}^s \longrightarrow \mathcal{A}$, $f_1, \dots, f_m : \{0, 1\}^s \longrightarrow [r]$ be r -surjective functions, where f_i s are possibly partial; m, r, s arbitrary integer parameters. Then the following bounds hold.

- (i) (cf. Lemma 3.3) If $k(C) \leq d_1(\mathcal{A})$ then $S_T(\tau(C, \mathcal{A}, \vec{F}, \vec{f})) \leq O(|C| \cdot 2^{s(k(C)+1)})$.
- (ii) $\tilde{w}(\tau(C, \mathcal{A}, \vec{F}, \vec{f}) \vdash \emptyset) \geq \frac{r}{2} \cdot \min\{k(C), d_0(\mathcal{A})\}$.
- (iii) $S(\tau(C, \mathcal{A}, \vec{F}, \vec{f})) \geq \exp\left(\Omega\left(\frac{r^2}{s} \cdot \min\{k(C), d_0(\mathcal{A})\}\right)\right)$.

Proof. (Lemma 3.8)

Part 1. By formalizing the proof of Lemma 3.3. Let $k \stackrel{\text{def}}{=} k(C)$ and a_1, \dots, a_n be such that $C(a_1, \dots, a_n) = 1$ and $k(a) = k$. Assume for simplicity that $a_1 = \dots = a_k =$

1, $a_{k+1} = \dots = a_n = 0$. Fix arbitrary admissible vectors $\vec{a}_1 \stackrel{\text{def}}{=} \begin{pmatrix} a_{i1} \\ \vdots \\ a_{m1} \end{pmatrix}, \dots, \vec{a}_k \stackrel{\text{def}}{=}$

$\begin{pmatrix} a_{ik} \\ \vdots \\ a_{mk} \end{pmatrix}$ and, using the inequality $d_1(\mathcal{A}) \geq k$, pick up an arbitrary $i \in [m]$ (depending in general on $\vec{a}_1, \dots, \vec{a}_k$) such that $a_{i1} = a_{i2} = \dots = a_{ik} = 1$. We want to infer from $\tau(C, \mathcal{A}, \vec{F}, \vec{f})$ all clauses in the CNF expansion of

$$[\text{Column}_1 \neq \vec{a}_1] \vee \dots \vee [\text{Column}_k \neq \vec{a}_k] \vee [\text{Control}_i \neq c] \quad (3.6)$$

for all $c \in [r]$. It is fairly obvious how to do this efficiently in general Resolution. Namely, let V be the set of all nodes of the circuit C that are evaluated to 1 by the assignment $(1^k, 0^{n-k})$. Then we may proceed by induction on the construction of C and subsequently infer

$$([\text{Column}_1 = \vec{a}_1] \wedge \dots \wedge [\text{Column}_k = \vec{a}_k] \wedge [\text{Control}_i = c]) \supset z_{iv}^c$$

for all $v \in V$ until we reach v_{fin} .

In order to get a *tree-like* proof, however, we should employ a dual (top-down) strategy. Namely, enumerate the set V in some order which is consistent with the topology of C : $V = \langle v_1 = p_1, v_2 = p_2, \dots, v_k = p_k, v_{k+1}, v_{k+2}, \dots, v_t = v_{\text{fin}} \rangle$; all wires between vertices in V go from the left to the right. Then, by a reverse induction on $\mu = t, t-1, \dots, k$ we infer (all clauses in the CNF expansion of) $[\text{Control}_i = c] \supset (\bar{z}_{i,v_1}^c \vee \dots \vee \bar{z}_{i,v_\mu}^c)$. For $\mu = t$ this is (a weakening of) (3.5), and for the inductive step we resolve with the appropriate axiom in (3.4). When we descend to $[\text{Control}_i = c] \supset (\bar{z}_{i,p_1}^c \vee \dots \vee \bar{z}_{i,p_k}^c)$, we consecutively resolve with the corresponding axioms (3.3) to get rid of \bar{z}_{i,p_j}^c and arrive at (3.6). Clearly, this resolution inference of every individual clause in (3.6) is tree-like and has size $O(|C|)$.

Finally, for every $i \in [m]$, every clause in the variables $\{y_i^\nu \mid 1 \leq \nu \leq s\}$ appears in one of the CNFs resulting from the predicates $\{[\text{Control}_i \neq c] \mid c \in [r]\}$ or in (3.2), and every clause in the variables $\{x_j^\nu \mid 1 \leq \nu \leq s\}$ appears in one of $[\text{Column}_j \neq \vec{a}_j]$. This gives us an obvious tree-like refutation of the set of clauses (3.2), (3.6) that has size $O(2^{s(k+1)})$. Combining this refutation with previously constructed inferences of (3.6) from $\tau(C, \mathcal{A}, \vec{F}, \vec{f})$, we get the desired upper bound.

Part 2. We follow the general strategy proposed in [10]. Note that every one of the axioms (3.2)-(3.5) “belongs” to an uniquely defined row; let Row_i be the set of axioms in $\tau(C, \mathcal{A}, \vec{F}, \vec{f})$ that correspond to the row i . For a clause D , let $\mu(D)$ be the smallest cardinality of $I \subseteq [m]$ such that $\cup \{\text{Row}_i \mid i \in I\}$ (semantically) implies D . $\mu(D)$ is subadditive, that is $\mu(D) \leq \mu(D_1) + \mu(D_2)$ whenever D is obtained from D_1, D_2 via a single application of the resolution rule. It is also obvious that $\mu(A) = 1$ for any axiom $A \in \tau(C, \mathcal{A}, \vec{F}, \vec{f})$.

We claim that $\mu(\emptyset) > d_0(\mathcal{A})$. Indeed, fix any $I \subseteq [m]$ with $|I| \leq d_0(\mathcal{A})$. We need to construct an assignment that satisfies all axioms in $\cup \{\text{Row}_i \mid i \in I\}$. Pick \vec{a} accordingly to Definition 3.6 in such a way that $\forall i \in I (a_i = 0)$. Assign every x_j^ν to α_j^ν , where $\alpha_j^1, \dots, \alpha_j^s$ is an arbitrary vector such that $F_j(\alpha_j^1, \dots, \alpha_j^s) = \vec{a}$; assign y_i^ν in an arbitrary way with the only requirement that they satisfy (3.2), and assign all z -variables to 0. This assignment will satisfy all axioms in $\cup \{\text{Row}_i \mid i \in I\}$ which proves $\mu(\emptyset) > d_0(\mathcal{A})$.

Thus, any resolution refutation of $\tau(C, \mathcal{A}, \vec{F}, \vec{f})$ must contain a clause D with $\frac{1}{2}d_0(\mathcal{A}) \leq \mu(D) \leq d_0(\mathcal{A})$, and we only need to show that this implies $\tilde{w}(D) \geq \frac{r}{2}$.

$\min\{k(C), d_0(\mathcal{A})\}$. Fix $I \subseteq [m]$ such that $\cup \{\text{Row}_i \mid i \in I\}$ semantically implies D and $|I|$ is minimal with this property; $\frac{1}{2}d_0(\mathcal{A}) \leq |I| \leq d_0(\mathcal{A})$.

If for every $i \in I$ at least one of the following two events is true:

1. the clause D contains at least r variables amongst $\{y_i^\nu \mid \nu \in [s]\}$
2. for every control $c \in [r]$ the clause D contains at least one variable amongst $\{z_{iv}^c \mid v \text{ is a node}\}$,

then we are done. Indeed, if h is the number of indices i for which 1) is true, then $w_y(D) \geq rh$. For those $i \in I$ for which 1) does not hold, we apply 2) to conclude $\min_{c \in [r]} w_c(D) \geq (|I| - h)$, and altogether we have $\tilde{w}(D) \geq w_y(D) + r \cdot \min_{c \in [r]} w_c(D) \geq r \cdot |I| \geq \frac{r}{2}d_0(\mathcal{A})$.

Thus, suppose that for some $i_0 \in I$ neither of these two is true. In particular, there exists a control $c_0 \in [r]$ such that no variable of the form $z_{i_0, v}^{c_0}$ appears in D . Fix an arbitrary assignment α that satisfies all axioms in $\{\text{Row}_i \mid i \in I \setminus \{i_0\}\}$ and falsifies D (such an assignment exists due to the minimality of $|I|$).

Let J_0 consist of those $j \in [n]$ for which the clause D contains at least r variables from $\{x_j^\nu \mid \nu \in [s]\}$. If $|J_0| \geq k(C)$, we are also done. If this is not the case, we will show how to alter the assignment α so that it will satisfy all axioms in $\cup \{\text{Row}_i \mid i \in I\}$ (including Row_{i_0}) but still will falsify D , and this will give us the contradiction.

According to Definition 3.6, there exists $\vec{a} \in \mathcal{A}$ such that $a_i = 0$ for all $i \in I$. We alter α as follows.

Step 1. Using that F_j is r -surjective, we change for every $j \notin J_0$ the values of the variables $\{x_j^\nu \mid \nu \in [s]\}$ not appearing in D in such a way that $F_j(x_j^1, \dots, x_j^s) = \vec{a}$.

Step 2. Using the fact that f_{i_0} is r -surjective, change the values of variables $\{y_{i_0}^\nu \mid \nu \in [s]\}$ not appearing in D in such a way that $f_{i_0}(y_{i_0}^1, \dots, y_{i_0}^s) = c_0$. Finally, re-assign every $z_{i_0, v}^{c_0}$ to the value computed by the node v on the characteristic vector of the set J_0 . Note that $z_{i_0, p_j}^{c_0}$ is set to 1 for $j \in J_0$ whereas $z_{i_0, v_{\text{fin}}}^{c_0}$ is set to 0 since $|J_0| < k(C)$.

We claim that this altered assignment α' satisfies all axioms in $\cup \{\text{Row}_i \mid i \in I\}$. Indeed, we made sure in our construction that it satisfies all axioms in Row_{i_0} of types (3.2), (3.4), (3.5), and for $i \in I \setminus \{i_0\}$ axioms of these types are satisfied since we have not touched any variable appearing in them. Thus, we only have to check the axioms (3.3). If $j \in J_0$ and $i \neq i_0$, this axiom has not been touched, and if $j \notin J_0$, it becomes satisfied because of the first step in our construction of α' , and due to the condition $a_i = 0$ ($i \in I$). Finally, if $j \in J_0$ and $i = i_0$, the axiom (3.3) gets satisfied during the second step (in which we set $z_{i_0, p_j}^{c_0}$ to 1).

But α' also falsifies D since we have not touched variables appearing in it. This contradiction with the fact that $\{\text{Row}_i \mid i \in I\}$ implies D completes the proof of part 2.

Part 3. We apply the standard argument of width-reducing restrictions (cf. [8]). For doing this we observe that the CNFs of the form $\tau(C, \mathcal{A}, \vec{F}, \vec{f})$ behave well with respect to certain restrictions. Namely, let $d \leq r$ and $R \subseteq [r]$ be an arbitrary set of controls. Denote by $\mathcal{R}_{d, R}$ the set of all restrictions that arbitrarily assign to a Boolean value d variables in every one of the groups $\{x_j^\nu \mid \nu \in [s]\}$, $\{y_i^\nu \mid \nu \in [s]\}$ with $j \in [n]$, $i \in [m]$ as well as all the variables z_{iv}^c with $c \notin R$. Then it is easy to see that for $\rho \in \mathcal{R}_{d, R}$, every non-trivial clause in $\tau(C, \mathcal{A}, \vec{F}, \vec{f})|_\rho$, after a suitable re-enumeration of variables and controls, contains a subclause from $\tau(C, \mathcal{A}, \vec{F}|_\rho, (\vec{f}|_\rho)|_R)$ (the *partial* function $(f_i|_\rho)|_R$ is obtained from $f_i|_\rho$ by restricting its domain to $\{y_i \mid f_i|_\rho(y_i) \in R\}$ and range to R).

Pick now ρ uniformly at random from $\mathcal{R}_{r/2, \mathbf{R}}$, where \mathbf{R} is picked at random from $[r]^{r/2}$. Then $F_j|_{\rho}, (f_i|_{\rho})|_{\mathbf{R}}$ will be $(r/2)$ -surjective. Therefore, by the already proven part 2, for every refutation P of $\tau(C, \mathcal{A}, \vec{F}, \vec{f})$, $\rho(P)$ will contain a clause of controlled width

$$\Omega(r \cdot \min\{k(C), d_0(\mathcal{A})\}) \quad (3.7)$$

with probability 1.

It is easy to see, however, that every clause D whose controlled width $\tilde{w}(D)$ is that large, is killed (that is, set to 1 and hence removed from the proof) by ρ with probability

$$1 - \exp\left(-\Omega\left(\frac{r^2}{s} \cdot \min\{k(C), d_0(\mathcal{A})\}\right)\right).$$

Indeed, according to Definition 3.7, either $w_x(D)$ or $w_y(D)$ or $r \cdot \min_{c \in [r]} w_c(D)$ is bounded from below by a quantity of the form (3.7). Let $w_{x,j}(D)$ be the number of variables in the corresponding group that appear in D , so that $w_x(D) = \sum_{j \in [n]} w_{x,j}(D)$. Then the probability that D is not killed by variables in the j th group is $\exp\left(-\Omega\left(\frac{r \cdot w_{x,j}(D)}{s}\right)\right)$, and these events are independent so the probabilities of survival multiply to $\exp\left(-\Omega\left(\frac{r \cdot w_x(D)}{s}\right)\right)$. The case when $w_y(D)$ is large is treated in exactly the same way, and the case when $r \cdot \min_{c \in [r]} w_c(D)$ is large is even simpler since for *every* choice of \mathbf{R} , the number of assigned z -variables is at least $\frac{r}{2} \cdot \min_{c \in [r]} w_c(D)$ (and $s \geq r$).

Therefore, the size of P must be at least $\exp\left(\Omega\left(\frac{r^2}{s} \cdot \min\{k(C), d_0(\mathcal{A})\}\right)\right)$ since otherwise a random restriction ρ would have killed all such clauses with non-zero probability, which is impossible.

Lemma 3.8 is completely proved. \square

Proof. (Lemma 3.1)

Our construction of $\tau(C, m)$ proceeds in polynomial time as follows.

1. Let p be the smallest prime larger or equal than m . Since $m \leq p \leq 2m$, both bounds in Lemma 3.1 remain unchanged if we replace m by p or vice versa. Therefore, w.l.o.g. we may assume from the beginning that m itself is a prime. Let P_m be the $(m \times m)$ 0-1 *Paley matrix* given by $a_{ij} = 1$ if and only if $j \neq i$ and $(j-i)$ is a quadratic residue mod m . Let $\mathcal{A} \subseteq \{0, 1\}^m$ consist of all columns of P_m . Then $|\mathcal{A}| = m$ and $d_0(\mathcal{A}), d_1(\mathcal{A}) \geq \frac{1}{4} \log m$ (see e.g. [5]).

2. Fix any \mathbb{F}_2 -linear code $L \subseteq \{0, 1\}^{h \lceil \log m \rceil}$ of dimension $\lceil \log m \rceil$ that is computable (as a language) in time $m^{O(1)}$ and has minimal distance $\geq \lceil \log m \rceil$ ($h > 0$ is an absolute constant.) Consider the linear mapping $G : \{0, 1\}^{h \lceil \log m \rceil} \rightarrow \{0, 1\}^{\lceil \log m \rceil}$ dual to the inclusion $L \rightarrow \{0, 1\}^{h \lceil \log m \rceil}$ (that is, we fix in L an arbitrary basis $x_1, \dots, x_{\lceil \log m \rceil}$, and let $G(y) \stackrel{\text{def}}{=} (\langle x_1, y \rangle, \dots, \langle x_{\lceil \log m \rceil}, y \rangle)$.) By linear duality, the fact that L has minimal distance $\geq \lceil \log m \rceil$ is equivalent to $\lceil \log m \rceil$ -surjectivity of G . Set $r \stackrel{\text{def}}{=} \lceil \log m \rceil$ and $s \stackrel{\text{def}}{=} h \lceil \log m \rceil$. Consider arbitrary (poly-time computable) surjective mappings $\Pi : \{0, 1\}^{\lceil \log m \rceil} \rightarrow \mathcal{A}$, $\pi : \{0, 1\}^{\lceil \log m \rceil} \rightarrow [r]$, and let $F_j \stackrel{\text{def}}{=} \Pi \cdot G, f_i \stackrel{\text{def}}{=} \pi \cdot G$ for all i, j .

3. Construct $\tau(C, \mathcal{A}, \vec{F}, \vec{f})$. Note that the size of this CNF is polynomial in $|C|, m, 2^s$ which is polynomial in $|C|, m$ due to our choice of parameters.

At this point, Lemma 3.8 3 already implies (3.1) for $\tau(C, \mathcal{A}, \vec{F}, \vec{f})$. The only remaining problem is that a priori we do not have the condition $k(C) \leq d_1(\mathcal{A})$ needed for part 1 of Lemma 3.8. We circumvent this by a trick similar to one used in [3].

Namely, let τ_m be a fixed unsatisfiable (poly-time constructible) CNF with $S(\tau_m), S_T(\tau_m) = m^{\theta(\log m)}$ (for example, one can take a Tseitin tautology with $\theta((\log m)^2)$ variables) and such that its set of variables is disjoint from the set of variables of $\tau(C, \mathcal{A}, \vec{F}, \vec{f})$. We finally set $\tau(C, m) \stackrel{\text{def}}{=} \tau(C, \mathcal{A}, \vec{F}, \vec{f}) \wedge \tau_m$.

Since both $\tau(C, \mathcal{A}, \vec{F}, \vec{f})$ and τ_m satisfy the lower bound (3.1), Weak Feasible Disjunction Property (see e.g. [21] and the literature cited therein) for Resolution implies that $\tau(C, m)$ satisfies this bound, too. If $k(C) \leq \frac{1}{4} \log m$ then, since $d_1(\mathcal{A}) \geq \frac{1}{4} \log m$, we can apply Lemma 3.8 1 to get the required upper bound $S_T(\tau(C, m)) \leq |C| \cdot m^{O(k(C))}$. If, on the other hand, $k(C) \geq \frac{1}{4} \log m$, the required upper bound $S_T(\tau(C, m)) \leq m^{O(\log m)}$ simply follows from the upper bound for τ_m . This completes the proof of Lemma 3.1. \square

4. Self-improvement. In this section we complete the proof of Theorems 2.5, 2.7 by combining Lemma 3.1 with a (non-trivial) self-improvement technique. First we need to get rid of the dummy parameter m in the statement of Lemma 3.1.

LEMMA 4.1. *If either Resolution or tree-like Resolution is automatizable then there exists an absolute constant $h > 1$ and an algorithm Φ working on pairs $\langle C, k \rangle$, where C is a monotone circuit and k is an integer such that:*

1. *the running time of $\Phi(\langle C, k \rangle)$ is at most $\exp(O(k^2)) \cdot |C|^{O(1)}$;*
2. *if $k(C) \leq k$ then $\Phi(\langle C, k \rangle) = 1$;*
3. *if $k(C) \geq hk$ then $\Phi(\langle C, k \rangle) = 0$.*

Proof. Combining the reduction in Lemma 3.1 with an automatizing algorithm for either Resolution or tree-like Resolution, we get an integer-valued function $S(C, m)$ computable in time $(|C| \cdot m^{\min\{k(C), \log m\}})^{h_0}$ and such that

$$m^{\epsilon \cdot \min\{k(C), \log m\}} \leq S(C, m) \leq \left(|C| \cdot m^{\min\{k(C), \log m\}}\right)^{h_1}$$

for some absolute constants $\epsilon, h_0, h_1 > 0$. Set the constant h in the statement in such a way that

$$h^2 > \frac{h_1}{\epsilon}(h + 1). \quad (4.1)$$

Our algorithm Φ works as follows. We set

$$m \stackrel{\text{def}}{=} 2^{h \cdot \max\{k, \log |C|/k\}}.$$

Φ simulates $(|C| \cdot m^k)^{h_0}$ steps in the computation of $S(C, m)$, outputs 1 if the computation halts within this time and its result $S(C, m)$ satisfies the inequality $S(C, m) \leq (|C| \cdot m^k)^{h_1}$, and outputs 0 in all other cases.

Our choice of m ensures that $m^k \leq \exp(O(k^2)) \cdot |C|^{O(1)}$, which implies property 1).

Since $\log m \geq hk \geq k$, under the assumption $k(C) \leq k$ the limitations we have imposed on the running time and the output value of the algorithm Φ are less stringent than the bounds known of the underlying algorithm computing $S(C, m)$. This observation implies property 2).

Finally, using again the inequality $\log m \geq hk$, $k(C) \geq hk$ implies that $S(C, m) \geq m^{\epsilon hk}$, and elementary calculations show that, along with (4.1), this gives us $S(C, m) > (|C| \cdot m^k)^{h_1}$. Thus, if $k(C) \geq hk$, the algorithm Φ outputs the value 0.

Lemma 4.1 is proved. \square

Proof. (Theorem 2.5)

First, we extract from Lemma 4.1 an algorithm which meets the bound on the running time and achieves the ratio of approximation h . For that we consecutively run the algorithm Φ from that lemma on the inputs $\langle C, 1 \rangle, \dots, \langle C, k \rangle, \dots$, and output the first value k for which we get the answer 0.

Combining this algorithm with the self-improving reduction from Proposition 2.4 (for $d = \lceil \frac{1}{\epsilon} \ln h \rceil$), we get approximating algorithm with the required properties. \square

In the established terminology, what we have seen so far under the assumption of automatizability of (tree-like) Resolution is a PTAS (polynomial time approximation scheme) for MMCSA in the context of parameterized complexity (the latter referring to the term $\exp(k(C)^{O(1)})$ in the bound on the running time). Unfortunately, our PTAS is not efficient (see the discussion in Section 2.2) as the reduction from Proposition 2.4 blows up the size of the circuit. The task of converting an arbitrary PTAS into an EPTAS seems to be hopeless in general even in the context of parameterized complexity (where it appears to be easier). We nonetheless can perform it (in the latter context) for the specific problem MMCSA using a much trickier self-improvement construction. This construction (that completes the proof of our main Theorem 2.7) might be of independent interest, and its idea is roughly as follows.

We need to improve the approximation ratio of the algorithm Φ in Theorem 2.5 from (say) 2 to (say) $\left(1 + \frac{1}{\sqrt{k}}\right)$, and the straightforward way of doing this is by iteratively applying Proposition 2.4 (say) $d = \sqrt{k}$ times. The corresponding reduction will map any circuit $C(x_1, \dots, x_n)$ into an n -ary tree of C -gates, and of depth d , and the resulting increase in size is too costly to us. What we basically show is that we can circumvent this by replacing the tree with a *random* DAG of the same depth d and of width polynomial in n .

Proof. (Theorem 2.7)

Let C be a monotone circuit in n variables and k be an integer such that

$$10 \leq k \leq \epsilon(\log n / \log \log n)^2 \quad (4.2)$$

for a sufficiently small constant $\epsilon > 0$ (we will remark later how to get rid of this condition). Our goal is to construct in polynomial time a randomized monotone circuit $\pi(C, k)$ and an integer $\alpha(k)$ (deterministically depending only on k) such that α is recursive and the following conditions hold:

$$k(C) \leq k \implies \mathbf{P}[k(\pi(C, k)) \leq \alpha(k)] = 1; \quad (4.3)$$

$$k(C) \geq k + 1 \implies \mathbf{P}[k(\pi(C, k)) \geq 2\alpha(k)] \geq 1/2. \quad (4.4)$$

First, we apply to C the reduction from Proposition 2.4 with $d = 2$ that maps the range $[k, k + 1]$ to $[k^2, k^2 + 2k + 1]$. Re-denoting k^2 back to k , we may assume w.l.o.g. that in (4.4) we have a stronger premise:

$$k(C) \geq k + 2\sqrt{k} \implies \mathbf{P}[k(\pi(C, k)) \geq 2\alpha(k)] \geq 1/2. \quad (4.5)$$

Now comes our main reduction. Let N, d be two parameters (to be specified later). The randomized circuit $\pi(C, N, d)$ in (nN) variables consists of d layers. Each

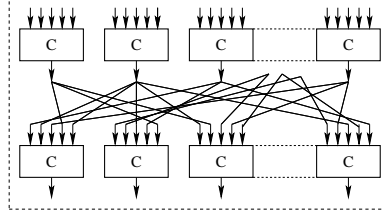


FIG. 4.1. One layer of $\pi(C, N, d)$

layer consists of N independent copies of the circuit C (see Fig. 4.1); thus, it has (nN) inputs and N outputs. We connect input nodes at the $(i+1)$ st level to output nodes at the i th level at random. Finally, we pick up an arbitrary output node at the last d th level and declare it to be the output of the whole circuit $\pi(C, N, d)$.

Clearly, this construction is polynomial in $|C|, N, d$. Also, an obvious induction on d shows that

$$k(\pi(C, N, d)) \leq k(C)^d \quad (4.6)$$

with probability 1. In order to get a lower bound on $k(\pi(C, N, d))$, we need the following easy lemma. It is of course yet another version of the well-known fact that a random (bipartite) graph makes an extremely good expander.

LEMMA 4.2. Let $\chi : [N] \times [n] \rightarrow [N]$ be a randomly chosen function, and k, a be any parameters. Then $\mathbf{P}[\exists V \in [N]^k (|\chi(V \times [n])| \leq kn - a)] \leq N^k \cdot \left(\frac{4k^2 n^2}{N}\right)^a$.

Proof. (Lemma 4.2)

This event takes place if and only if there exist $V \in [N]^k$ and disjoint $D_1, \dots, D_r \subseteq V \times [n]$ such that $|D_1|, \dots, |D_r| \geq 2$, $\sum_{i=1}^r (|D_i| - 1) = a$ and $\chi|_{D_i} = \text{const}$ for all $i \in [r]$. Since the two first properties imply $\sum_{i=1}^r |D_i| \leq 2a$, the overall number of all choices of $\langle V, D_1, \dots, D_r \rangle$ does not exceed $N^k \cdot (2kn)^{2a}$. On the other hand, for every fixed choice we have

$$\mathbf{P}[\chi|_{D_1} = \text{const}, \dots, \chi|_{D_r} = \text{const}] = \frac{N^r}{N^{\sum_{i=1}^r |D_i|}} = N^{-a}.$$

Lemma 4.2 follows. \square

Now we can complete the description of our reduction. Namely, we set $N \stackrel{\text{def}}{=} n^3$, $d \stackrel{\text{def}}{=} \sqrt{k}$ and let $\pi(C, k) = \pi(C, n^3, \sqrt{k})$, $\alpha(k) \stackrel{\text{def}}{=} k^{\sqrt{k}}$.

(4.3) follows from (4.6).

In order to check (4.5), denote by $\chi_i : [N] \times [n] \rightarrow [N]$ the function used for connecting input nodes at the $(i+1)$ st level of $\pi(C, n^3, \sqrt{k})$ to the output nodes at the i th level. Let $k_i \stackrel{\text{def}}{=} (k + \sqrt{k})^{d-i}$. Let us call $\pi(C, N, d)$ *bad* if for at least one of these functions χ_i there exists a set V of circuits at the $(i+1)$ st level such that $|V| = k_{i+1}$ and $|\chi_i(V \times [n])| \leq k_{i+1}(n - \sqrt{k})$. Using Lemma 4.2 and (4.2), we get the

bound

$$\begin{aligned}
\mathbf{P}[\pi(C, N, d) \text{ is bad}] &\leq \sum_{i=1}^{d-1} N^{k_{i+1}} \cdot \left(\frac{4k_{i+1}^2 n^2}{N} \right)^{\sqrt{k} \cdot k_{i+1}} \\
&= \sum_{i=1}^{d-1} \left(\frac{4k_{i+1}^2}{n^{1-3/\sqrt{k}}} \right)^{\sqrt{k} \cdot k_{i+1}} \\
&\leq \sum_{i=1}^{d-1} (1/3)^{\sqrt{k} \cdot k_{i+1}} \leq 1/2.
\end{aligned}$$

On the other hand, it is easy to see by induction on $i = d, \dots, 1$ that if $k(C) \geq k + 2\sqrt{k}$ and $\pi(C, N, d)$ is good then every satisfying assignment a should satisfy at least k_i output nodes at the i th level. Indeed, the base $i = d$ is obvious ($k_d = 1$). For the inductive step, assume that a satisfies the output nodes of a set V of circuits at the $(i+1)$ th level, $|V| = k_{i+1}$. Then at least $(k + 2\sqrt{k}) \cdot k_{i+1}$ input nodes to these circuits should be satisfied. Since χ_i is good, there are at most $\sqrt{k} \cdot k_{i+1}$ collisions between the $(k + 2\sqrt{k}) \cdot k_{i+1}$ wires leading to these input nodes from the i th level. Therefore, at least $(k + 2\sqrt{k}) \cdot k_{i+1} - \sqrt{k} \cdot k_{i+1} = k_i$ output nodes at the i th level should be satisfied.

In particular, at the first level we will have $\geq (k + \sqrt{k})^{d-1}$ satisfied circuits and $\geq (k + 2\sqrt{k}) \cdot (k + \sqrt{k})^{\sqrt{k}-1} > 2\alpha(k)$ satisfied input nodes. This completes the proof that our probabilistic reduction $\pi(C, k)$ has the properties (4.3), (4.5) (and, as we already remarked, improving (4.5) to (4.4) takes one more easy step).

Now we finish the proof of Theorem 2.7. Suppose that either Resolution or tree-like Resolution is automatizable. Since WEIGHTED MONOTONE CIRCUIT SATISFIABILITY is $\mathbf{W[P]}$ -complete (see [17, Chapter 13]), we only have to show that the language $\{\langle C, k \rangle \mid k(C) \leq k\}$ is in $co\text{-FPR}$. Given an input $\langle C, k \rangle$ we check the condition (4.2). If it is violated, we apply a straightforward brute-force algorithm with running time $O(|C| \cdot n^k) \leq |C| \cdot f(k) \cdot n^9$ for some recursive f . Otherwise we simply combine our probabilistic reduction $\langle \pi, \alpha \rangle$ with the deterministic algorithm for deciding whether $k(\pi(C, k)) \leq \alpha(k)$ or $k(\pi(C, k)) \geq 2\alpha(k)$ provided by Theorem 2.5. Theorem 2.7 is completely proved. \square

5. Open Problems. The main problem left open by this paper is whether general Resolution is quasi-automatizable. Since the width algorithm by Ben-Sasson and Wigderson [10] finds a resolution refutation of any unsatisfiable CNF τ in time $n^{O(w(\tau) \cdot \ell)}$, a negative solution to this problem must involve a construction of a broad and “tractable” family of CNF τ for which $S(\tau)$ is much smaller than $2^{w(\tau) \cdot \ell}$. Such families are not so easy to come by (e.g. our techniques involve showing the *opposite* in the proof of Lemma 3.8 3), although some progress toward this goal was reported in [6].

As we already mentioned in Section 1.1, the same paper [6] also proposed an interesting notion of weak automatizability. Namely, a proof system P is *weakly automatizable* if there exists any automatizable proof system that polynomially simulates P . Is Resolution *weakly* automatizable (under any reasonable complexity assumptions in case of negative answer?) [6] showed that this is equivalent to another important open question in Proof Complexity, namely if the system $Res(2)$ has Feasible Interpolation Property (for definitions see e.g. [22].)

We were not able to de-randomize the proof of Lemma 4.2. In the terminology of [1], we need explicit constructions of $(N \times N)$ 0-1 matrices that would be $(k, n, n -$

$O(1)$)-expanders for $n \geq N^{\Omega(1)}$ and an arbitrary function $k = k(N)$ tending to infinity. Explicit constructions based on Ramanujan graphs seem to give only $(k, n, n - k^\epsilon)$ -expanders for any *fixed* ϵ which is not sufficient for our purposes. Can we weaken the hardness assumption in Theorem 2.7 to $\mathbf{W[P]} \neq \mathbf{FPT}$ by an explicit construction of better expanders (or by using any other means)?

Acknowledgement. The second author is greatly indebted to all three anonymous referees of the journal version of this paper for their constructive criticism and many useful remarks and suggestions.

REFERENCES

- [1] M. ALEKHNovich, E. BEN-SASSON, A. RAZBOROV, AND A. WIGDERSON, *Pseudorandom generators in propositional proof complexity*, SIAM Journal on Computing, 34 (2004), pp. 67–88.
- [2] M. ALEKHNovich, M. BRAVERMAN, V. FELDMAN, A. R. KLIVANS, AND T. PITASSI, *Learnability and automatizability*, in Proceedings of the 45th IEEE Symposium on Foundations of Computer Science, 2004, pp. 621–630.
- [3] M. ALEKHNovich, S. BUSS, S. MORAN, AND T. PITASSI, *Minimum propositional proof length is NP-hard to linearly approximate*, Journal of Symbolic Logic, 66 (2001), pp. 171–191.
- [4] M. ALEKHNovich AND A. RAZBOROV, *Satisfiability, branch-width and Tseitin tautologies*, in Proceedings of the 43rd IEEE Symposium on Foundations of Computer Science, 2002, pp. 593–603.
- [5] N. ALON, *Tools from higher algebra*, in Handbook of Combinatorics, vol. II, Elsevier, 1995, ch. 32.
- [6] A. ATSERIAS AND M. BONET, *On the automatizability of resolution and related propositional proof systems*, Information and Computation, 189 (2004), pp. 182–201.
- [7] C. BAZGAN, *Schémas d'approximation et complexité paramétrée*, Rapport de stage de DEA d'Informatique à Orsay, 1995.
- [8] P. BEAME AND T. PITASSI, *Simplified and improved resolution lower bounds*, in Proceedings of the 37th IEEE FOCS, 1996, pp. 274–282.
- [9] ———, *Propositional proof complexity: Past, present and future*, in Current Trends in Theoretical Computer Science: Entering the 21st Century, G. Paun, G. Rozenberg, and A. Salomaa, eds., World Scientific Publishing, 2001, pp. 42–70.
- [10] E. BEN-SASSON AND A. WIGDERSON, *Short proofs are narrow - resolution made simple*, Journal of the ACM, 48 (2001), pp. 149–169.
- [11] M. BONET, C. DOMINGO, R. GAVALDÁ, A. MACIEL, AND T. PITASSI, *Non-automatizability of bounded-depth Frege proofs*, Computational Complexity, 13 (2004), pp. 47–68.
- [12] M. BONET AND N. GALESI, *A study of proof search algorithms for Resolution and Polynomial Calculus*, in Proceedings of the 40th IEEE FOCS, 1999, pp. 422–431.
- [13] M. BONET, T. PITASSI, AND R. RAZ, *On interpolation and automatization for Frege systems*, SIAM Journal on Computing, 29 (2000), pp. 1939–1967.
- [14] M. CESATI AND L. TREVISAN, *On the efficiency of polynomial time approximation schemes*, Information Processing Letters, 64 (1997), pp. 165–171.
- [15] S. A. COOK AND A. R. RECKHOW, *The relative efficiency of propositional proof systems*, Journal of Symbolic Logic, 44 (1979), pp. 36–50.
- [16] I. DINUR AND M. SAFRA, *On the hardness of approximating label-cover*, Information Processing Letters, 89 (2004), pp. 247–254.
- [17] R. DOWNEY AND M. FELLOWS, *Parameterized Complexity*, Springer-Verlag, 1998.
- [18] K. IWAMA, *Complexity of finding short resolution proofs*, in Proceedings of the 22nd International Symposium on the Mathematical Foundations of Computer Science (Bratislava, August '97), Lecture Notes in Computer Science 1295, P. Ružička I.Prvara, ed., Springer-Verlag, 1997, pp. 309–318.
- [19] J. KRAJÍČEK, *Proof complexity*, in European congress of mathematics (ECM), Stockholm, Sweden, June 27–July 2, 2004, A. Laptev, ed., European Mathematical Society, 2005, pp. 221–231.
- [20] J. KRAJÍČEK AND P. PUDLK, *Some consequences of cryptographical conjectures for S_2^1 and EF*, Information and Computation, 142 (1998), pp. 82–94.
- [21] P. PUDLK, *On reducibility and symmetry of disjoint NP-pairs*, Theoretical Computer Science, 295 (2003), pp. 323–339.

- [22] N. SEGERLIND, *The complexity of propositional proofs*, Bulletin of Symbolic Logic, 13 (2007), pp. 417–481.